META II

META II is a compiler writing language which consists of syntax equations resembling Backus normal form and into which instructions to output assembly language commands are inserted. Compilers have been written in this language for VALGOL I and VALGOL II. The former is a simple algebraic language designed for the purpose of illustrating META II. The latter contains a fairly large subset of ALGOL 60.

The method of writing compilers which is given in detail in the paper may be explained briefly as follows. Each syntax equation is translated into a recursive subroutine which tests the input string for a particular phrase structure, and deletes it if found. Backup is avoided by the extensive use of factoring in the syntax equations. For each source language, an interpreter is written and programs are compiled into that interpretative language.

META II is not intended as a standard language which everyone will use to write compilers. Rather, it is an example of a simple working language which can give one a good start in designing a compiler-writing compiler suited to his own needs. Indeed, the META II compiler is written in its own language, thus lending itself to modification.

History

The basic ideas behind META II were described in a series of three papers by Schmidt, Metcalf, and Schorre. These papers were presented at the 1963 National A.C.M. Convention in Denver, and represented the activity of the Working Group on Syntax-Directed Compilers of the Los Angeles SIGPLAN. The methods used by that group are similar to those of Glennie and Conway, but differ in one important respect. Both of these researchers expressed syntax in the form of diagrams, which they subsequently coded for use on a computer. In the case of META II, the syntax is input to the computer in a notation resembling Backus normal form. The method of syntax analysis discussed in this paper is entirely different from the one used by Irons and Bastian. All of these methods can be traced back to the mathematical study of natural languages, as described by Chomsky.

Syntax Notation

The notation used here is similar to the meta language of the ALGOL 60 report. Probably the main difference is that this notation can be keypunched. Symbols in the target language are represented as strings of characters, surrounded by quotes. Metalinguistic variables have the same form as identifiers in ALGOL, viz., a letter followed by a sequence of letters or digits.

Items are written consecutively to indicate concatenation and separated by a slash to indicate alternation. Each equation ends with a semicolon which, due to keypunch limitations, is represented by a period followed by a comma. An example of a syntax equation is:

```
LOGICALVALUE = '.TRUE' / '.FALSE' ,
```

In the versions of ALGOL described in this paper the symbols which are usually printed in boldface type will begin with periods, for example:

```
.PROCEDURE . TRUE . IF
```

To indicate that a syntactic element is optional, it may be put in alternation with the word .EMPTY. For example:

```
SUBSECONDARY = '* ' PRIMARY / .EMPTY . ,
SECONDARY = PRIMARY SUBSECONDARY . 3
```

By factoring, these two equations can be written as a single equation.

```
SECONDARY = PRIMARY( '* ' PRIMARY / .EMPTY) . ,
```

The definition of identifier is the same in META II as in ALGOL, viz., a letter followed by a sequence of letters or digits. The definition of a string is changed because of the limited character set available on the usual keypunch. In ALGOL, strings are surrounded by opening and closing quotation marks, making it possible to have quotes within a string. The single quotation mark on the keypunch is unique, imposing the restriction that a string in quotes can contain no other quotation marks.

The definition of number has been radically changed. The reason for this is to cut down on the space required by the machine subroutine which recognizes numbers. A number is considered to be a string of digits which may include imbedded periods, but may not begin or end with a period; moreover, periods may not be adjacent. The use of the subscript 10 has been eliminated.

Now we have enough of the syntax defining features of the META II language so that we can consider a simple example in some detail.

The example given here is a set of four syntax equations for defining a very limited class of algebraic expressions. The two operators, addition and multiplication, will be represented by + and * respectively. Multiplication takes precedence over addition; otherwise precedence is indicated by parentheses. Some examples are:

```
1.3-1
```
A
A + B
A + B * C
(A + B) * C

The syntax equations which define this class of expressions are as follows:

EX = \{ID / ('EX1')\},
EX = EX3 ($ \{•*\ EX2 / .EMPTRY\}),
EX = EX2 ($ \{•+\ EX1 / .EMPTRY\}.

EX is an abbreviation for expression. The
last equation, which defines an expression of order 1, is considered the main equation. The equa-
tions are read in this manner. An expression of order 3 is defined as an identifier or an open
parenthesis followed by an expression of order 1
followed by a closed parenthesis. An expression
of order 2 is defined as an expression of order 3,
which may be followed by a star which is followed
by an expression of order 2. An expression
of order 1 is defined as an expression of order 2,
which may be followed by a plus which is followed
by an expression of order 1.

Although sequences can be defined recursive-
ly, it is more convenient and efficient to have a
special operator for this purpose. For example,
we can define a sequence of the letter A as fol-
loows:

SEQA = $ 'A'.

The equations given previously are rewritten using
the sequence operator as follows:

EX3 = .ID / ('EX1')',
EX2 = EX3 ($ \{•*\ EX2 / .EMPTRY\}),
EX1 = EX2 ($ \{•+\ EX1 / .EMPTRY\}.

Output
Up to this point we have considered the
notation in META II which describes object lan-
guage syntax. To produce a compiler, output com-
mands are inserted into the syntax equations.
Output from a compiler written in META II is al-
ways in an assembly language, but not in the as-
sembly language for the 1401. It is for an in-
terpreter, such as the interpreter I call the
META II machine, which is used for all compilers,
or the interpreters I call the VALGOL I and VAL-
GOL II machines, which obviously are used with
their respective source languages. Each machine
requires its own assembler, but the main differ-
ence between the assemblers is the operation code
table. Constant codes and declarations may also be
different. These assemblers all have the same
format, which is shown below.

LABEL CODE ADDRESS
1-6 8 -10 12- -70

An assembly language record contains either
a label or an op code of up to 3 characters, but
never both. A label begins in column 1 and may
extend as far as column 70. If a record contains
an op code, then column 1 must be blank. Thus
labels may be any length and are not attached to
instructions, but occur between instructions.
To produce output beginning in the op code
field, we write .OUT and then surround the infor-
mation to be reproduced with parentheses. A
string is used for literal output and an asterisk
to output the special symbol just found in the
input. This is illustrated as follows:

EX3 = .ID .OUT(′ID ! *) / (′EX1′),
EX2 = EX3 $ (′*′ EX2 / .EMPTRY),
EX1 = EX2 $ (′+′ EX1 / .EMPTRY).

To cause output in the label field we write
LABEL followed by the item to be output. For
example, if we want to test for an identifier
and output it in the label field we write:

.LABEL *

The META II compiler can generate labels of the
form A01, A02, A03, ... A99, B01, .... To
cause such a label to be generated, one uses *1 or
*2. The first time *1 is referred to in any
syntax equation, a label will be generated and
assigned to it. This same label is output when-
ever *1 is referred to within that execution of
the equation. The symbol *2 works in the same way.
Thus a maximum of two different labels may be
generated for each execution of any equation.
Repeated executions, whether recursive or externally
initiated, result in a continued sequence of gener-
ated labels. Thus all syntax equations con-
tribute to the one sequence. A typical example
in which labels are generated for branch commands
is now given.

IFSTATEMENT = ′IF′ EXP ′THEN′ .OUT(′BFP ! #1)
STATEMENT ′ELSE′ .OUT(′B ′ #2).LABEL #1
STATEMENT .LABEL #2,

The op codes BFP and B are orders of the
VALGOL I machine, and stand for "branch false and
pop" and "branch" respectively. The equation also
contains references to two other equations which
are not explicitly given, viz., EXP and STATEMENT.

VALGOL I - A Simple Compiler Written in META II

Now we are ready for an example of a compil-
er written in META II. VALGOL I is an extremely
simple language, based on ALGOL 60, which has been
designed to illustrate the META II compiler.

The basic information about VALGOL I is giv-
en in figure 1 (the VALGOL I compiler written in
META II) and figure 2 (order list of the VALGOL I
machine). A sample program is given in figure 3.
After each line of the program, the VALGOL I com-
mands which the compiler produces from that line
are shown, as well as the absolute interpretive
language produced by the assembler. Figure 4 is
output from the sample program. Let us study the
compiler written in META II (figure 1) in more
detail.

The identifier PROGRAM on the first line indi-
cates that this is the main equation, and that
group control goes there first. The equation for PRIM-
ARY is similar to that of EX3 in our previous
example, but here numbers are recognized and re-
produced with a "load literal" command. TERM is
what has previously EX2; and EX1 what was pre-
viously EX1 except for recognizing minus for sub-
traction. The equation EXP defines the relation-
al operator "equal", which produces a value of 0
or 1 by making a comparison. Notice that this is handled just like the arithmetic operators but with a lower precedence. The conditional branch commands, "branch true and pop" and "branch false and pop", which are produced by the equations defining UNTILST and CONDITIONALST respectively, will test the top item in the stack and branch accordingly.

The "assignment statement" defined by the equation for ASSIGNST is reversed from the convention in ALGOL 60, i.e., the location into which the computed value is to be stored is on the right. Notice also that the equal sign is used for the assignment statement and that period equal (\(=\)) is used for the relation discussed above. This is because assignment statements are more numerous in typical programs than equal compares, and so the simpler representation is chosen for the more frequently occurring.

The omission of statement labels from the VALGOL I and VALGOL II seems strange to most programmers. This was not done because of any difficulty in their implementation, but because of a dislike for statement labels on the part of the author. I have programmed for several years without using a single label, so I know that they are superfluous from a practical, as well as from a theoretical, standpoint. Nevertheless, it would be too much of a digression to try to justify this point here. The "until statement" has been added to facilitate writing loops without labels.

The "conditional" statement is similar to the one in ALGOL 60, but here the "else" clause is required.

The equation for "input/output", IOST, involves two commands, "edit" and "print". The words EDIT and PRINT do not begin with periods so that they will look like subroutines written in code. "EDIT" copies the given string into the print area, with the first character in the print position which is computed from the given expression. "PRINT" will print the current contents of the print area and then clear it to blanks. Giving a print command without previous edit commands results in writing a blank line.

IDEQ1 and IDEQ2 are given to simplify the syntax equations for DEC (declaration). Notice in the definition of DEC that a branch is given around the data.

From the definition of BLOCK it can be seen that what is considered a compound statement in ALGOL 60 is, in VALGOL I, a special case of a block which has no declaration.

In the definition of statement, the test for an IOST precedes that for an ASSIGNST. This is necessary, because if this were not done the words PRINT and EDIT would be mistaken as identifiers and the compiler would try to translate "input/output" statements as if they were "assignment" statements.

Notice that a PROGRAM is a block and that a standard set of commands is output after each program. The "halt" command causes the machine to stop on reaching the end of the outermost block, which is the program. The operation code SP is generated after the "halt" command. This is a completely 1401-oriented code, which serves to set a word mark at the end of the program. It would not be used if VALGOL I were implemented on a fixed word-length machine.

**How the META II Compiler Was Written**

Now we come to the most interesting part of this project, and consider how the META II compiler was written in its own language. The interpreter called the META II machine is not a much longer 1401 program than the VALGOL I machine. The syntax equations for META II (figure 5) are fewer in number than those for the VALGOL I machine (figure 1).

The META II compiler, which is an interpretive program for the META II machine, takes the syntax equations given in figure 5 and produces an assembly language version of this same interpretive program. Of course, to get this started, I had to write the first compiler-writing compiler by hand. After the program was running, it could produce the same program as written by hand. Someone always asks if the compiler really produced exactly the program I had written by hand and I have to say that it was "almost" the same program. I followed the syntax equations and tried to write just what the compiler was going to produce. Unfortunately I forgot one of the redundant instructions, so the results were not quite the same. Of course, when the first machine-produced compiler compiled itself the second time, it reproduced itself exactly.

The compiler originally written by hand was for a language called META I. This was used to implement the improved compiler for META II. Sometimes, when I wanted to change the metalanguage, I could not describe the new metalanguage directly in the current metalanguage. Then an intermediate language was created—one which could be described in the current language and in which the new language could be described. I thought that it might sometimes be necessary to modify the assembly language output, but it seems that it is always possible to avoid this with the intermediate language.

The order list of the META II machine is given in figure 6.

All subroutines in META II programs are recursive. When the program enters a subroutine a stack is pushed down by three cells. One cell is for the exit address and the other two are for labels which may be generated during the execution of the subroutine. There is a switch which may be set or reset by the instructions which refer to the input string, and this is the switch referred to by the conditional branch commands.

The first thing in any META II machine program is the address of the first instruction. During the initialization for the interpreter, this address is placed into the instruction counter.

**VALGOL II Written in META II**

VALGOL II is an expansion of VALGOL I, and serves as an illustration of a fairly elaborate programming language implemented in the META II system. There are several features in the VALGOL II machine which were not present in the
VALGOL I machine, and which require some explanation. In the VALGOL II machine, addresses as well as numbers are put in the stack. They are marked appropriately so that they can be distinguished at execution time.

The main reason that addresses are allowed in the stack is that, in the case of a subscripted variable, an address is the result of a computation. In an assignment statement each left member is compiled into a sequence of code which leaves an address on top of the stack. This is done for simple variables as well as subscripted variables, because the philosophy of this compiler writing system has been to compile everything in the most general way. A variable, simple or subscripted, is always compiled into a sequence of instructions which leaves an address on top of the stack. The address is not replaced by its contents until the actual value of the variable is needed, as in an arithmetic expression.

A formal parameter of a procedure is stored either as an address or as a value which is computed when the procedure is called. It is up to the load command to go through any number of indirect addresses in order to place the address of a number onto the stack. An argument of a procedure is always an algebraic expression. In case this expression is a variable, the value of the formal parameter will be an address computed upon entering the procedure; otherwise, the value of the formal parameter will be a number computed upon entering the procedure.

The operation of the load command is now described. It causes the given address to be put on top of the stack. If the content of this top item happens to be another address, then it is replaced by that other address. This continues until the top item on the stack is the address of something which is not an address. This allows for formal parameters to refer to other formal parameters to any depth.

No distinction is made between integer and real numbers. An integer is just a real number whose digits right of the decimal point are zero. Variables initially have a value called "undefined", and any attempt to use this value will be indicated as an error.

An assignment statement consists of any number of left parts followed by a right part. For each left part there is compiled a sequence of commands which puts an address on top of the stack. The right part is compiled into a sequence of instructions which leaves on top of the stack either a number or the address of a number. Following the instruction for the right part there is a sequence of store commands, one for each left part. The first command of this sequence is "save and store", and the rest are "plain" store commands. The "save and store" puts the number which is on top of the stack (or which is referred to by the address on top of the stack) into a register called SAVE. It then stores the contents of SAVE in the address which is held in the next to top position of the stack. Finally it pops the top two items, which it has used, off of the stack. The number, however, remains in SAVE for use by the following store commands. Most assignment statements have only one left part, so "plain" store commands are seldom produced, with the result that the number put in SAVE is seldom used again.

The method for calling a procedure can be explained by reference to illustrations 1 and 2. The arguments which are in the stack are moved to their place at the top of the procedure. If the

Illustration 1

Storage Map for VALGOL II Procedures

Illustration 2

Map of the Stack Relating to Procedure Calls

D1.3-4
above the flag, and continuing to the top of the stack. Arguments are moved into the appropriate places at the top of the procedure being called. An error message is given if the number of arguments in the stack does not correspond to the number of places in the procedure. Finally, the old flag address, which is just below the procedure address in the stack, is put in the flag register. The exit address replaces the address of the procedure in the stack, and all the arguments, as well as the flag, are popped out.

There are just two op codes which affect the flag register. The code "load flag" puts a flag into the stack, and the code "call" takes one out.

The library function "WHOLE" truncates a real number. It does not convert a real number to an integer, because no distinction is made between them. It is substituted for the recommended function "ENTIER" primarily because truncation takes fewer machine instructions to implement. Also, truncation seems to be used more frequently. The procedure ENTIER can be defined in VALGOL II as follows:

```plaintext
.PROCEDURE ENTIER(X).
  .IF 0 .L~ X .THEN WHOLE(X) .ELSE
  .~ ~oLE(x) --(WHOLE(X) -1)
```

The "for statement" in VALGOL II is not the same as it is in ALGOL. Exactly one list element is required. The "step .. until" portion of the element is mandatory, but the "while" portion may be added to terminate the loop immediately upon some condition. The iteration continues so long as the value of the variable is less than or equal to the maximum, irrespective of the sign of the increment. Illustration 3 is an example of a typical "for statement". A flow chart of this statement is given in illustration 4.

```
.FOR I = 0 .STEP 1 .UNTIL N .DO
  (statement)

  SET
  LD I
  FLP A92
  BFP A92
  LDL 0
  SST
  B A93
  A92
  LDL 1
  ADS A93
  A93
  RSR
  LD N
  LRQ
  BFP A94
  (statement)
  RST
  B A91
```

Illustration 3
Compilation of a typical "for statement" in VALGOL II

Illustration 4
Flow chart of the "for statement" given in figure 12

Figure 7 is a listing of the VALGOL II compiler written in META II. Figure 8 gives the order list of the VALGOL II machine. A sample program to take a determinant is given in figure 9.

Backup vs. No Backup

Suppose that, upon entry to a recursive subroutine, which represents some syntax equation, the position of the input and output are saved. When some non-first term of a component is not found, the compiler does not have to stop with an indication of a syntax error. It can back-up the input and output and return false. The advantages of backup are as follows:

1. It is possible to describe languages, using backup, which cannot be described without backup.
2. Even for a language which can be described without backup, the syntax equations can often be simplified when backup is allowed.
The advantages claimed for non-backup are as follows:

1. Syntax analysis is faster.
2. It is possible to tell whether syntax equations will work just by examining them, without following through numerous examples.

The fact that rather sophisticated languages such as ALGOL and COBOL can be implemented without backup is pointed out by various people, including Conway, and they are aware of the speed advantages of so doing. I have seen no mention of the second advantage of no-backup, so I will explain this in more detail.

Basically one writes alternations in which each term begins with a different symbol. Then it is not possible for the compiler to go down the wrong path. This is made more complicated because of the use of "EMPTY". An optional item can never be followed by something that begins with the same symbol it begins with.

The method described above is not the only way in which backup can be handled. Variations are worth considering, as a way may be found to have the advantages of both backup and no-backup.

Further Development of META Languages

As mentioned earlier, META II is not presented as a standard language, but as a point of departure from which a user may develop his own META language. The term "META Language," with "META" in capital letters, is used to denote any compiler-writing language so developed.

The language which Schmidt implemented on the PDP-1 was based on META I. He has now implemented an improved version of this language for a Beckman machine.

Rutman has implemented LOGIK, a compiler for bit-time simulation, on the 7090. He uses a META language to compile Boolean expressions into efficient machine code. Schneider and Johnson have implemented META 3 on the IBM 7094, with the goal of producing an ALGOL compiler which generates efficient machine code. They are planning a META language which will be suitable for any block structured language. To this compiler-writing language they give the name META 4 (pronounced metaphor).

References

THE VALGOL I CORPILER WRITTEN IN META II LANGUAGE

### Figure 1

**SYNTAX PROGRAM**

- **PRIMARY** **ID** OUT**('LD')** ND /
  - **NUMBER** OUT**('LD')** ND /
  - '1' EXP 1 ND
- **TERM** PRIMARY **BN** PRIMARY OUT**('MLT')** 1 ND
- **EXPL** PRIMARY 1 ND
  - PRIMARY OUT**('ADD')** /
  - **TERM** OUT**('SUB')** 1 ND
- **EXP** EXP 1 ND
  - EXP OUT**('EQU')** / **EMPTY** 1 ND
- **ASSIGNS** EXP 1 ND
  - ID OUT**('ST')** 1 ND
- **UNTILST** 1 ND
  - **LABEL** PRIMARY 1 ND
  - EXP 1 ND
  - LABEL 1 ND
- **CONDITIONALST** 1 ND
  - **IF** EXP **THEN** OUT**('BFP')** 1 ND
  - **ELSE** OUT**('ST')** 1 ND
- **IOST** 1 ND
  - EDIT PRIMARY 1 ND
  - STRING OUT**('EDT')** 1 ND
- **PRINT** 1 ND
  - PNT OUT**('PNT')** 1 ND
- **IDSEQI** PRIMARY 1 ND
  - LABEL 1 ND
  - OUT**('BLK')** 1 ND
- **IDSEQ** IDSEQI 1 ND
  - ISEQ1 1 ND
- **DEC** 1 ND
  - REAL OUT**('DEC')** 1 ND
- **BLOCK** 1 ND
  - IDSEQ 1 ND
  - ST 1 ND
- **ST** 1 ND
  - BEGIN 1 ND
  - **END** 1 ND

A PROGRAM AS COMPILED FOR THE VALGOL I MACHINE

**Figure 3**

```plaintext
BEGIN
  REAL X ** 0 * X **
  B A01 0000 G 0012
  X
  B A02 0025 G 0004
  X
  B A03 0095 G 0025
  X
  B A04 0097

Halt
```

**MACHINE CODES**

- **LD** AAA LOAD
  - PUT THE CONTENTS OF THE ADDRESS AAA ON TOP OF THE STACK.
- **LOLD** NUMBER LOAD LITERAL
  - PUT THE GIVEN NUMBER ON TOP OF THE STACK.
- **ST** AAA STORE
  - STORE THE NUMBER WHICH IS ON TOP OF THE STACK INTO THE ADDRESS AAA AND POP UP THE STACK.
- **ADD** ADD
  - REPLACE THE TWO NUMBERS WHICH ARE ON TOP OF THE STACK WITH THEIR SUM.
- **SUB** SUBTRACT
  - SUBTRACT THE NUMBER WHICH IS ON TOP OF THE STACK FROM THE NUMBER WHICH IS NEXT TO THE TOP, THEN REPLACE THEM BY THIS DIFFERENCE.
- **MLT** MULTIPLY
  - REPLACE THE TWO NUMBERS WHICH ARE ON TOP OF THE STACK WITH THEIR PRODUCT.
- **EQU** EQUAL
  - COMPARE THE TWO NUMBERS ON TOP OF THE STACK. REPLACE THEM BY THE INTEGER 1, IF THEY ARE EQUAL, OR BY THE INTEGER 0, IF THEY ARE UNEQUAL.
- **B AAA** BRANCH TO THE ADDRESS AAA.
- **BFP AAA** BRANCH FALSE AND POP
  - BRANCH TO THE ADDRESS AAA IF THE TOP TERM IN THE STACK IS THE INTEGER 0. OTHERWISE, CONTINUE IN SEQUENCE. IN EITHER CASE, POP UP THE STACK.
- **BTP AAA** BRANCH TRUE AND POP
  - BRANCH TO THE ADDRESS AAA IF THE TOP TERM IN THE STACK IS NOT THE INTEGER 0. OTHERWISE, CONTINUE IN SEQUENCE. IN EITHER CASE, POP UP THE STACK.
- **EDT** STRING EDIT
  - ROUND THE NUMBER WHICH IS ON TOP OF THE STACK TO THE NEAREST INTEGER N. MOVE THE GIVEN STRING INTO THE PRINT AREA SO THAT ITS FIRST CHARACTER FALLS ON PRINT POSITION N. IN CASE THIS WOULD CAUSE CHARACTERS TO FALL OUTSIDE THE PRINT AREA, NO MOVEMENT TAKES PLACE.
- **PNT** PRINT
  - PRINT A LINE, THEN SPACE AND CLEAR THE PRINT AREA.
- **HLT** HALT
  - HALT.
- **SP** N SPACE
  - N = 1-9. CONSTANT CODE PRODUCING N BLANK SPACES.
- **BLK** NNN BLOCK
  - PRODUCES A BLOCK OF NNN EIGHT CHARACTER WORDS.
- **END** END
  - DENOTES THE END OF THE PROGRAM.

**Figure 2**

**ORDER LIST OF THE VALGOL I MACHINE**
THE NETA II COMPILER WRITTEN IN ITS OWN LANGUAGE

FIGURE 5

SYNTAX PROGRAM

OUTI = "*" OUTI("GM") / +"2" OUTI("GN") / ++" OUTI("CL") / STRING OUTI("C") ++

OUTPUT = "+" OUTI ("" / "LABEL" OUTI ("LB") OUTI ("OUTPUT") ++

B OUTI ("" / ++" OUTI("CL") / STRING OUTI("C") ++

EX3 = +1D OUTI("CL") / ++STRING OUTI("G") / +"2" OUTI("ID") / ++" NUMBER" OUTI("NUM") / ++STRING OUTI("TAG") / ++" LABEL" OUTI ("SET") / ++" LABEL" OUTI ("EL") ++

EX2 = (EXI OUTI("BF") / ++" OUTPUT") ++"

ST = +"ID" OUTI("AR") / ++" OUTPUT") ++"

PROGRAM = "+SYNTAX" +ID OUTI("ADR") ++

RETURN

SET SET

B AAA BRANCH

TST STRING TEST

ID IDENTIFIER

NUM NUMBER

SR STRING

CALL AAA CALL

RETURN TO THE EXIT ADDRESS, POPPING UP THE STACK BY ONE OR THREE CELLS ACCORDING TO THE FLAG. IF THE STACK IS POPPED BY ONLY ONE CELL, THEN CLEAR THE TOP TWO CELLS TO BLANKS, BECAUSE THEY WERE BLANK WHEN THE SUBROUTINE WAS ENTERED.

SET BRANCH SWITCH ON.

BRANCH UNCONDITIONALLY TO LOCATION AAA.

BRANCH TO LOCATION AAA IF SWITCH IS ON. OTHERWISE, CONTINUE IN SEQUENCE.

BRANCH TO LOCATION AAA IF SWITCH IS OFF. OTHERWISE, CONTINUE IN SEQUENCE.

BRANCH TO LOCATION AAA IF SWITCH IS OFF. OTHERWISE, CONTINUE IN SEQUENCE.

HALT IF SWITCH IS OFF. OTHERWISE, CONTINUE IN SEQUENCE.

OUTPUT THE VARIABLE LENGTH STRING GIVEN AS THE ARGUMENT. A BLANK CHARACTER WILL BE INSERTED IN THE OUTPUT FOLLOWING THE STRING.

OUTPUT THE LAST SEQUENCE OF CHARACTERS DELETED FROM THE INPUT STRING. THIS COMMAND MAY NOT FUNCTION PROPERLY IF THE LAST COMMAND WHICH CAUSES DELETION FAILED TO DO SO.

THIS CONCERNS THE CURRENT LABEL 1 CELL. IEY, THE NEXT TO TOP CELL IN THE STACK, WHICH IS EITHER CLEAR OR CONTAINS A GENERATED LABEL. IF CLEAR, GENERATE A LABEL AND PUT IT INTO THAT CELL. WHETHER THE LABEL HAS JUST BEEN PUT INTO THE CELL OR WAS ALREADY THERE, OUTPUT IT. FINALLY, INSERT A BLANK CHARACTER IN THE OUTPUT FOLLOWING THE LABEL.

SAME AS GNI, EXCEPT THAT IT CONCERNS THE CURRENT LABEL 2 CELL. IEY, THE TOP CELL IN THE STACK.

SET THE OUTPUT COUNTER TO CARD COLUMN 1, PUNCH CARD AND RESET OUTPUT COUNTER TO CARD COLUMN 8.

Figure 6.1

Figure 6.2

Figure 6.3
<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LD AAA</td>
<td>Load the address AAA on top of the stack.</td>
</tr>
<tr>
<td>LOL NUMBER LOAD LITERAL</td>
<td>Load the given number on top of the stack.</td>
</tr>
<tr>
<td>SET</td>
<td>Set the integer 1 on top of the stack.</td>
</tr>
<tr>
<td>RST</td>
<td>Reset the integer 0 on top of the stack.</td>
</tr>
<tr>
<td>ST</td>
<td>Store the contents of the register, stack, in the address which is on top of the stack, then pop up the stack.</td>
</tr>
<tr>
<td>ADS</td>
<td>Add to storage.</td>
</tr>
<tr>
<td>SST</td>
<td>Save and store.</td>
</tr>
<tr>
<td>RSR</td>
<td>Restore.</td>
</tr>
<tr>
<td>ADD</td>
<td>Add.</td>
</tr>
<tr>
<td>SUB</td>
<td>Subtract.</td>
</tr>
<tr>
<td>MLT</td>
<td>Multiply.</td>
</tr>
<tr>
<td>DIV</td>
<td>Divide.</td>
</tr>
<tr>
<td>NEG</td>
<td>Negate.</td>
</tr>
<tr>
<td>WHL</td>
<td>Whole.</td>
</tr>
<tr>
<td>NOT</td>
<td>Not.</td>
</tr>
<tr>
<td>LEQ</td>
<td>Less than or equal.</td>
</tr>
<tr>
<td>LES</td>
<td>Less than.</td>
</tr>
<tr>
<td>EQU</td>
<td>Equal.</td>
</tr>
<tr>
<td>B AAA</td>
<td>Branch to the address AAA.</td>
</tr>
<tr>
<td>BT AAA</td>
<td>Branch true.</td>
</tr>
<tr>
<td>BF AAA</td>
<td>Branch false.</td>
</tr>
<tr>
<td>BTP AAA</td>
<td>Branch true and pop.</td>
</tr>
<tr>
<td>BFP AAA</td>
<td>Branch false and pop.</td>
</tr>
</tbody>
</table>

**Figure 8.1**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>

**Figure 8.4**

**Order List of the Valsol II Machine Codes**

**Figure 8.2**

**Figure 8.3**

**Constant and Control Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SF N</td>
<td>Space.</td>
</tr>
<tr>
<td>BLK NNN</td>
<td>Block.</td>
</tr>
<tr>
<td>END</td>
<td>End.</td>
</tr>
</tbody>
</table>
EXAMPLE PROGRAM IN VALGOL II

```valgol
BEGIN
PROCEDURE DETERMINANT(A; N) **
BEGIN
PROCEDURE DUMP( ) **
BEGIN
REAL D **
FOR O • O *STEP 1 *UNTIL N-1 *DO
WRITE(* MATRIX(*) , N)
PRINT
END DUMP **
PROCEDURE ABS(X) .
ABS • IF 0 • X • THEN X • ELSE X **
REAL PRODUCT, FACTOR, TEMPERATURE.
PRODUCT • 1 •
FOR R • 0 *STEP 1 *UNTIL N-2
WHILE PRODUCT • 0 • DO • BEGIN
I • R **
FOR J • R *STEP 1 *UNTIL N-1 *DO
IF ABS(A(I, N+R)) • R • THEN
ABS(A(I, N+R) • R) • , THEN
PRODUCT • PRODUCT **
FOR J • R *STEP 1 *UNTIL N-1 *DO
FACTOR • A(I, N+R) / TEMPERATURE.
FOR J • R *STEP 1 *UNTIL N-1 *DO
A(I, N+R) • J • = A(I, N+R) • J
FACTOR • A(I, N+R) • J **
DUMP
END
END
FOR I • 0 *STEP 1 *UNTIL N-1 *DO
PRODUCT • PRODUCT • A(I, N+I) • I **
DETERMINANT • PRODUCT **
END DETERMINANT **
REAL N, T ** ARRAY MATRIX(1 • 0 • 24) **
UNTIL • FALSE • DO • BEGIN
EDIT(1), IF DETERMINANT OF • PRINT, PRINT **
REDA(S, 1) **
FOR W • 0 *STEP 1 *UNTIL N-1 *DO • BEGIN
READ(A(S, W), W)
WRITE(A(S, W), W **
PRINT • T • DETERMINANT(MATRIX, N) **
PRINT(S) ** PRINT • PRINT ** END
END PROGRAM
```

D1.3-23